

Docket: 740819-734

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

4/IDS  
P. Walk

In re National Phase Patent Application of )  
Akira ASAI et al. )  
International Application No. PCT/JP01/04344 ) Attn: US/DO/EO  
International Filing Date: May 23, 2001 )  
For: BIPOLAR TRANSISTOR AND )  
FABRICATION METHOD THEREOF ) Date: January 22, 2002

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents and Trademarks  
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99,  
it is requested that the reference listed on the attached Form PTO-1449 be made of  
record in the above-identified application.

A copy of this reference is submitted herewith in accordance with 37 C.F.R.  
1.98(a).

Respectfully submitted,



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